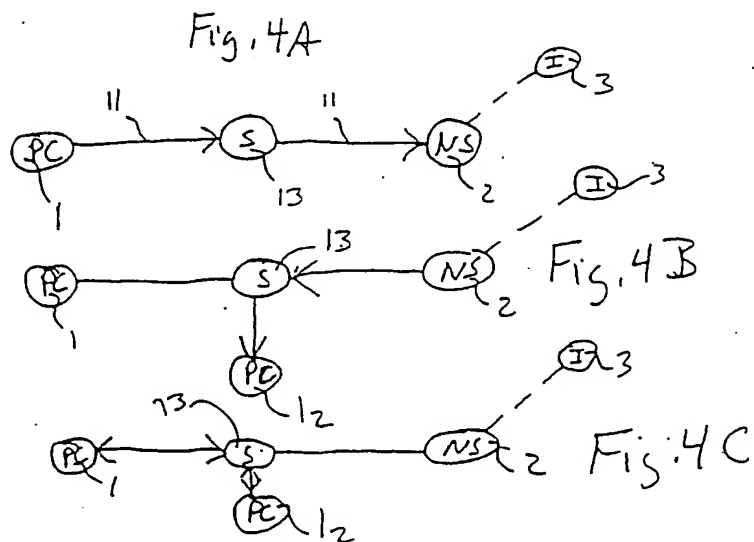
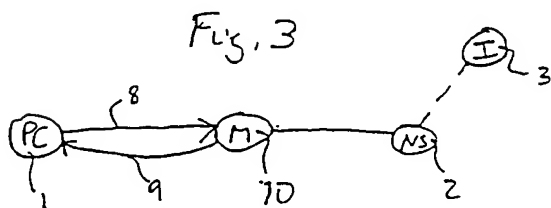
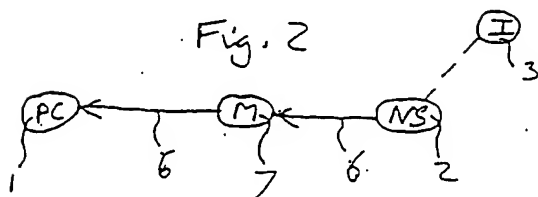
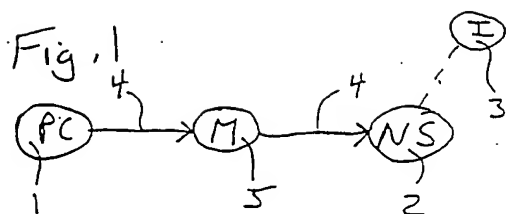


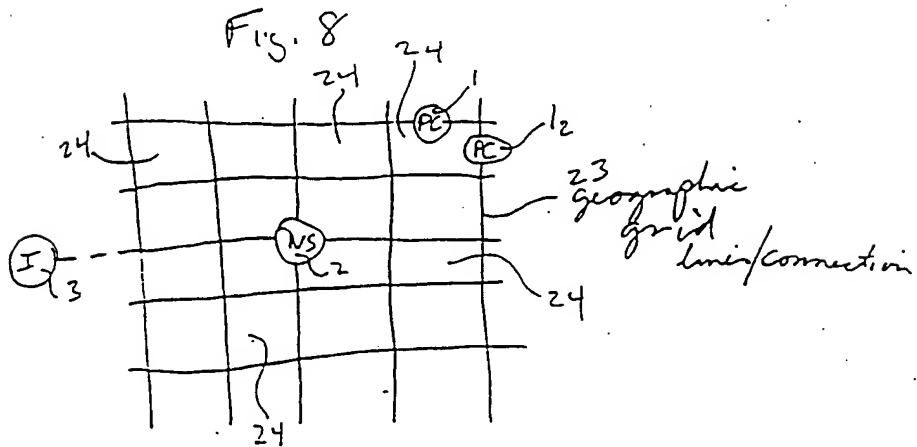
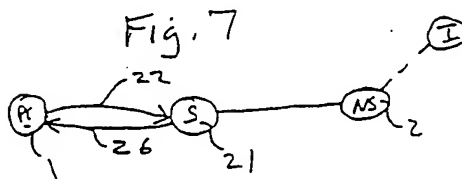
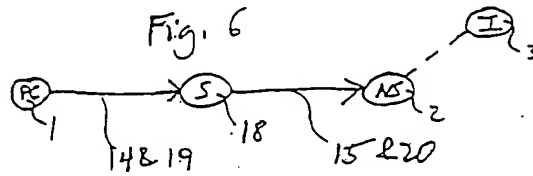
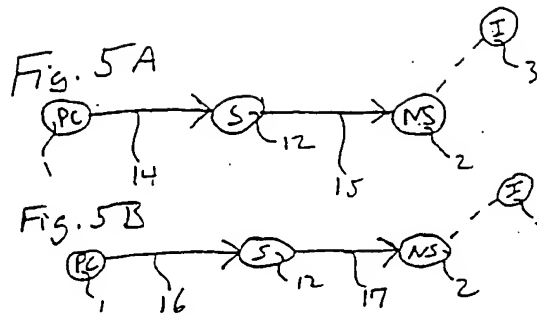


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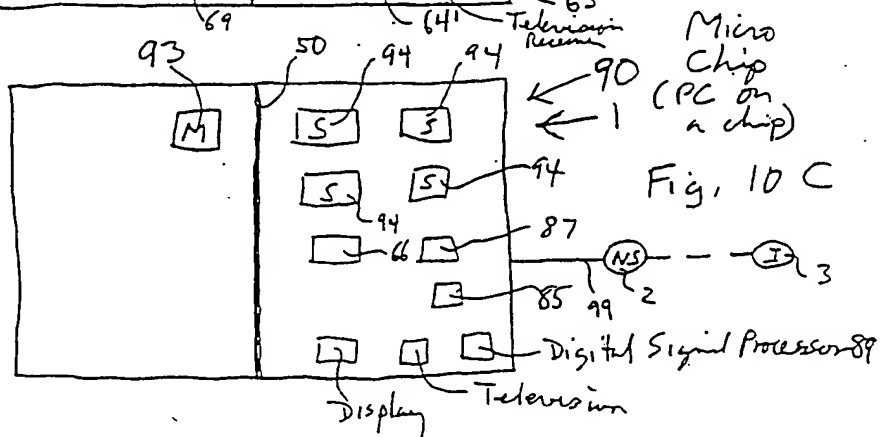
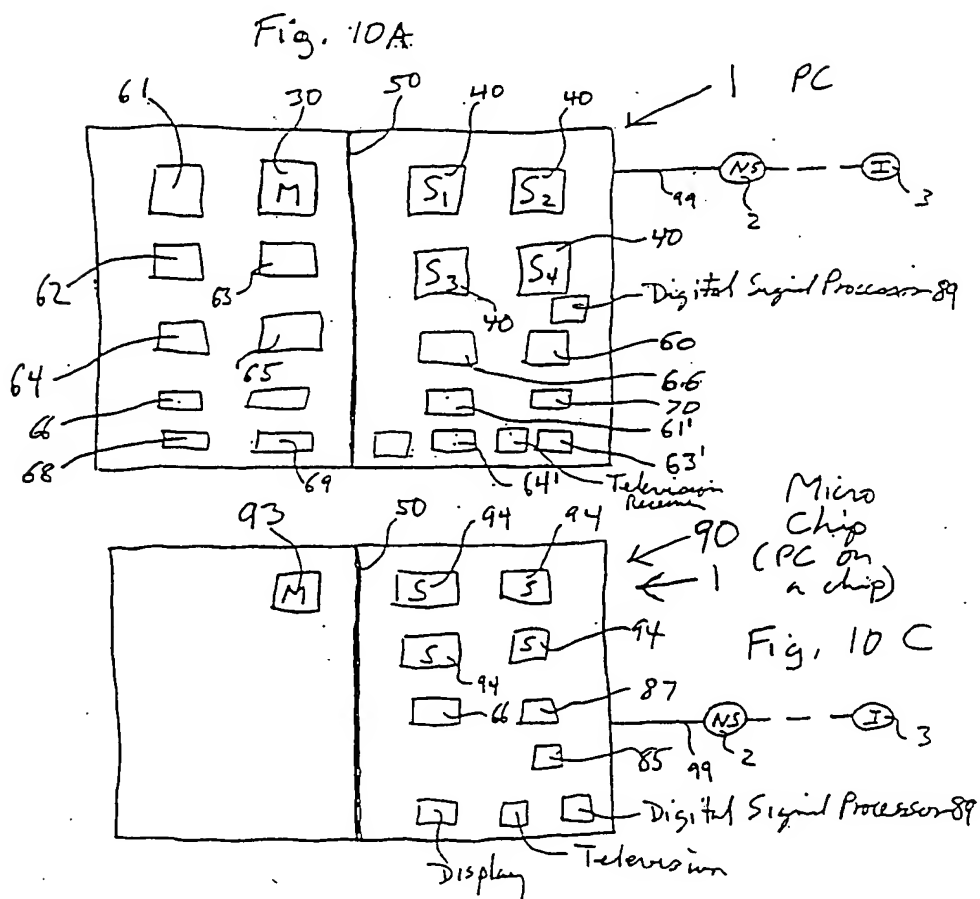
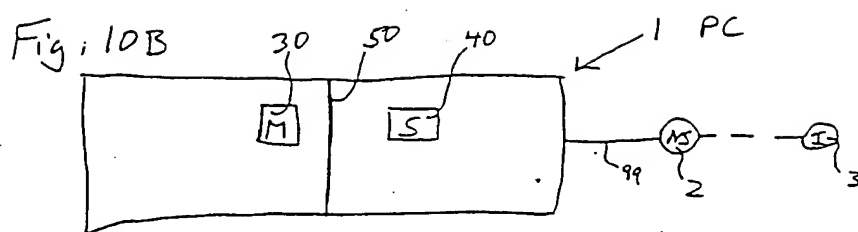
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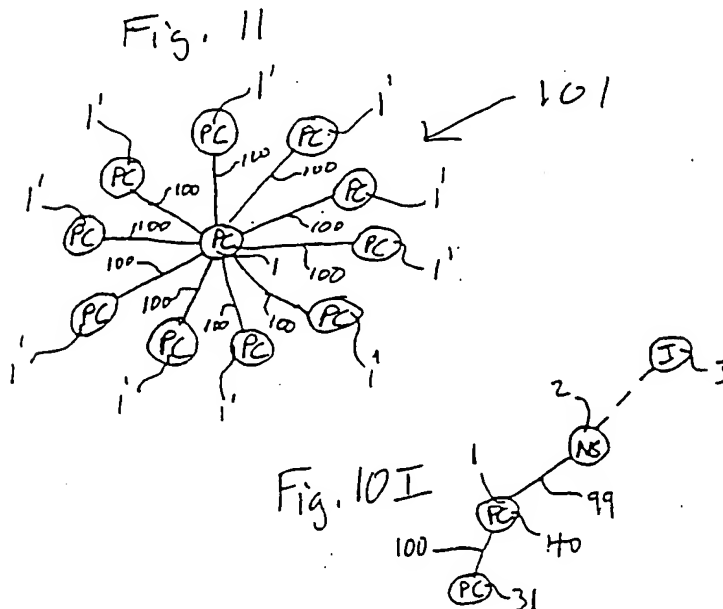
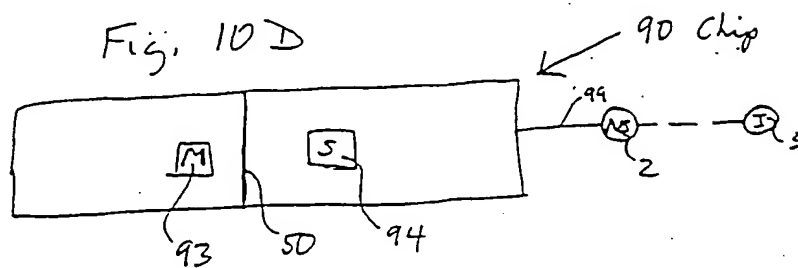
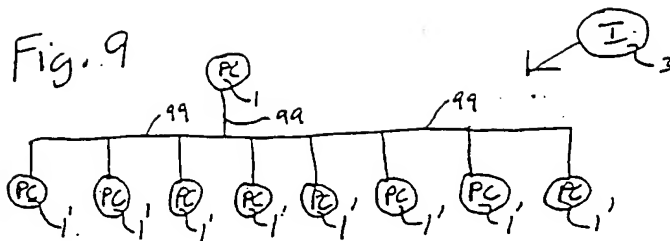


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Fig. 10E

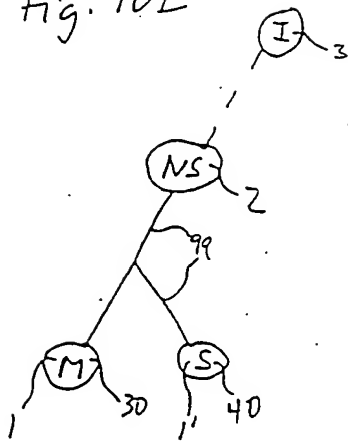


Fig. 10F



Fig. 10G

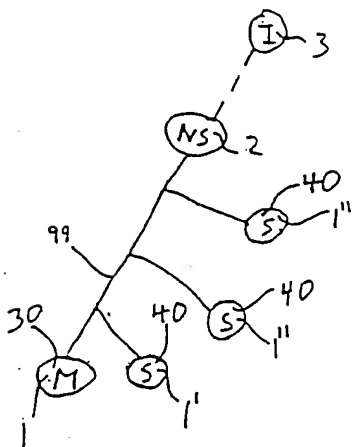
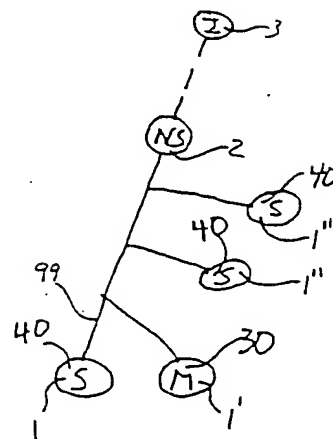
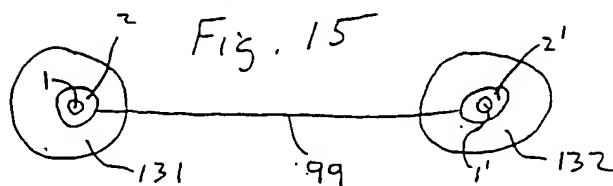
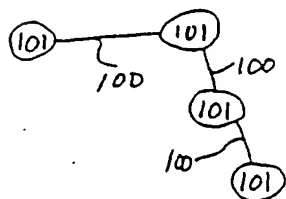
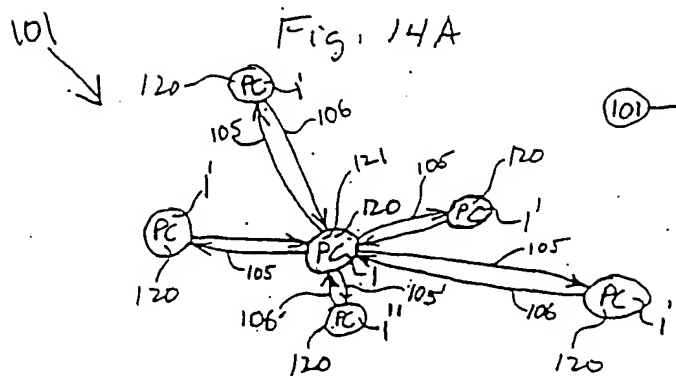
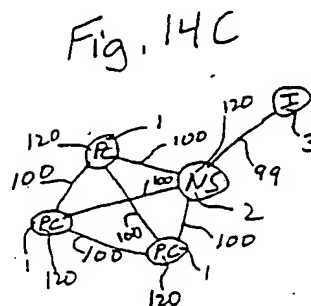
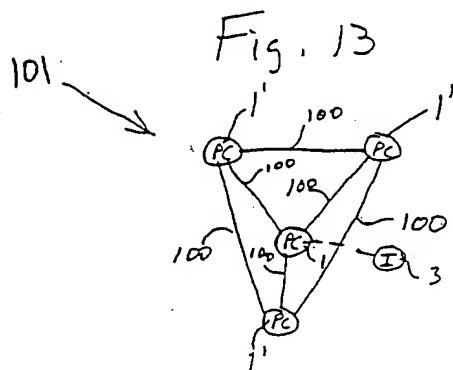
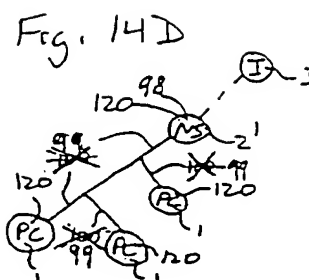
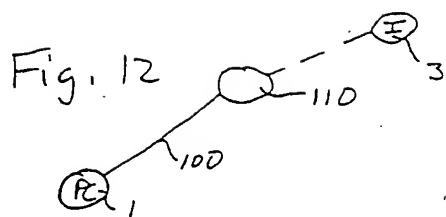
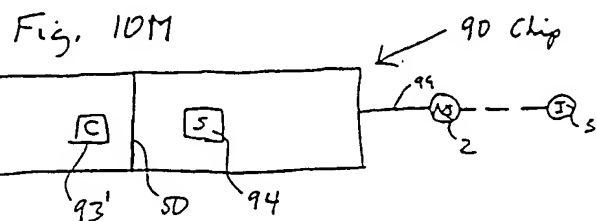
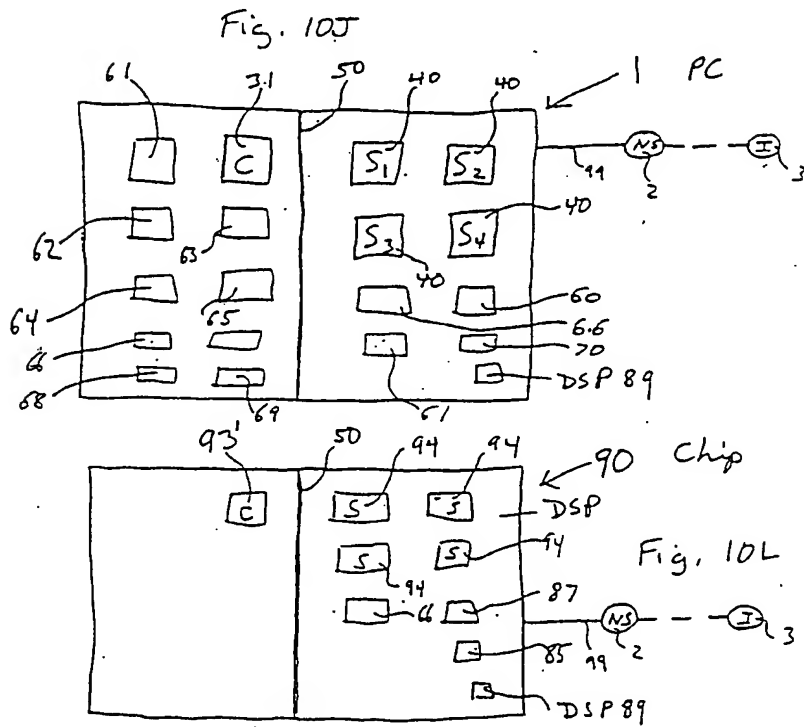
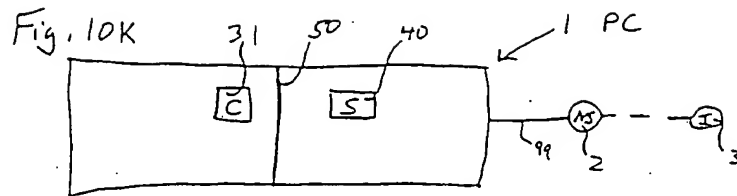


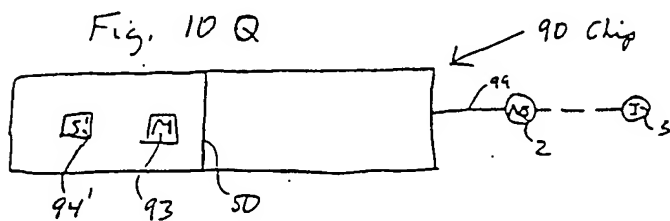
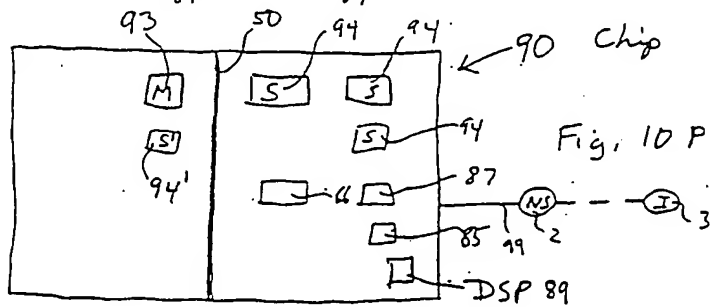
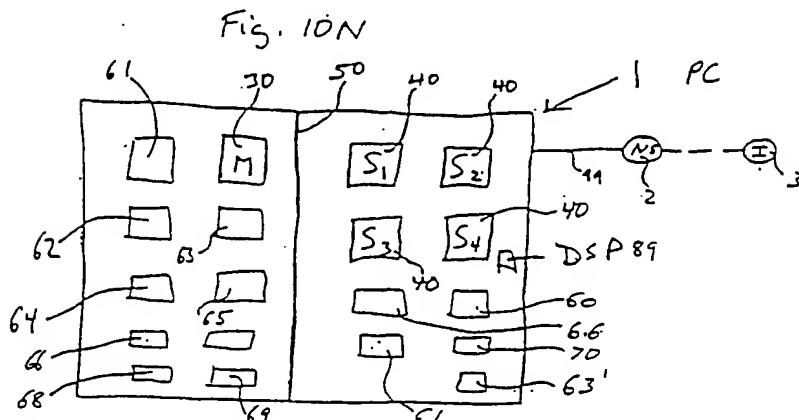
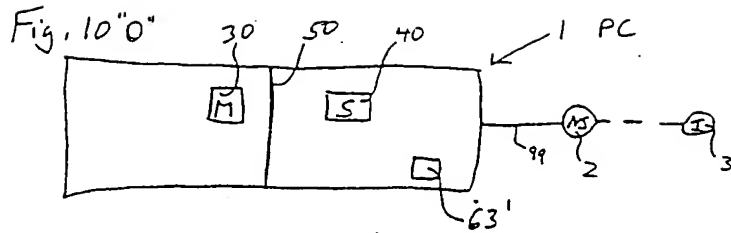
Fig. 10H



$$\begin{array}{r} 6 \overline{) 26} \\ \underline{31} \end{array}$$


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Fig. 16 A

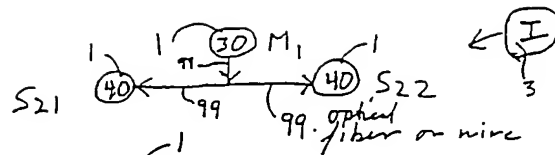


Fig. 16B

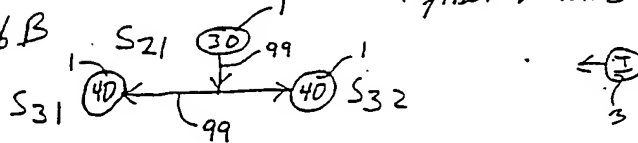


Fig. 16 C

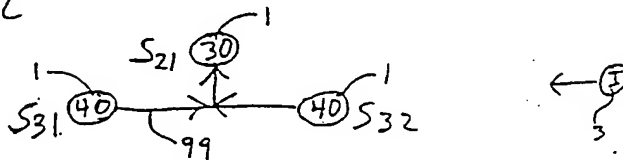


Fig. 16D

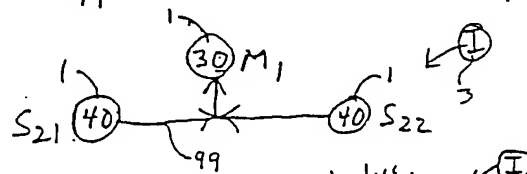


Fig. 16G



Fig. 16H

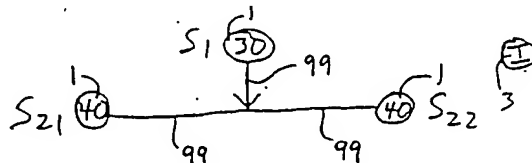


Fig. 16 I

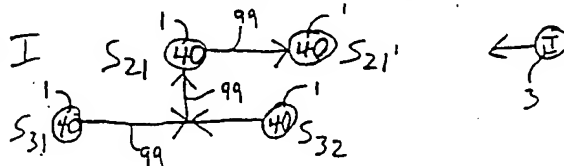


Fig. 16, J



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Fig. 16 E

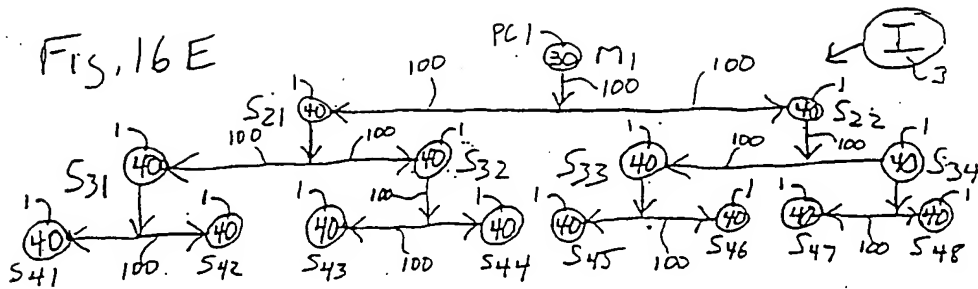


Fig. 16 F

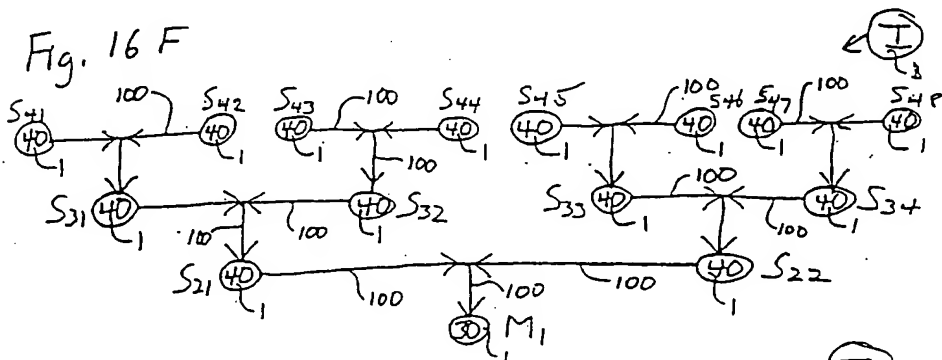
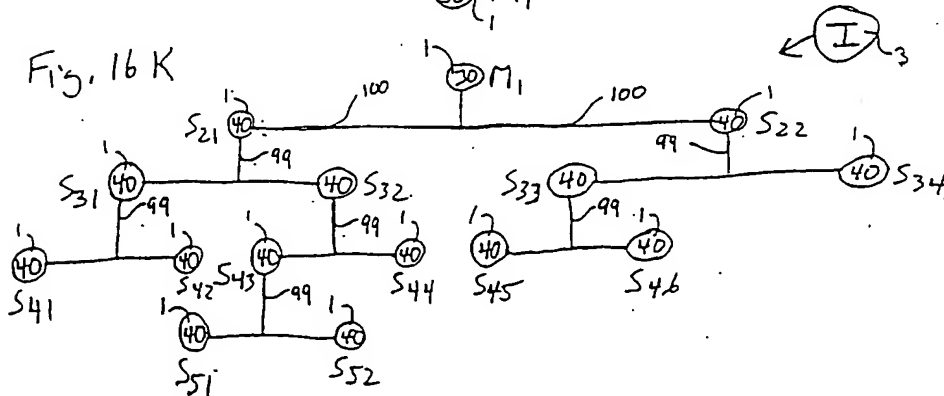


Fig. 16 K



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Fig. 16L

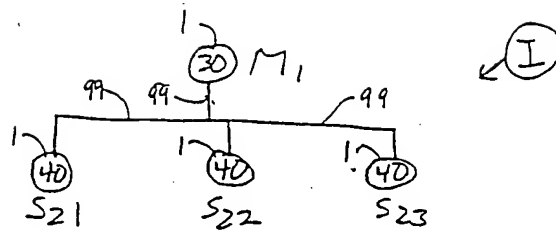


Fig. 16M

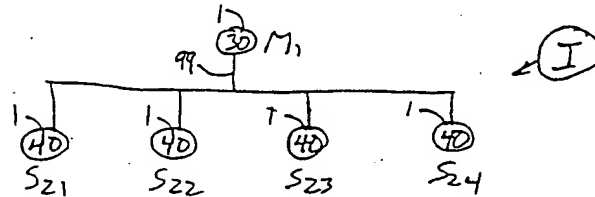


Fig. 16N

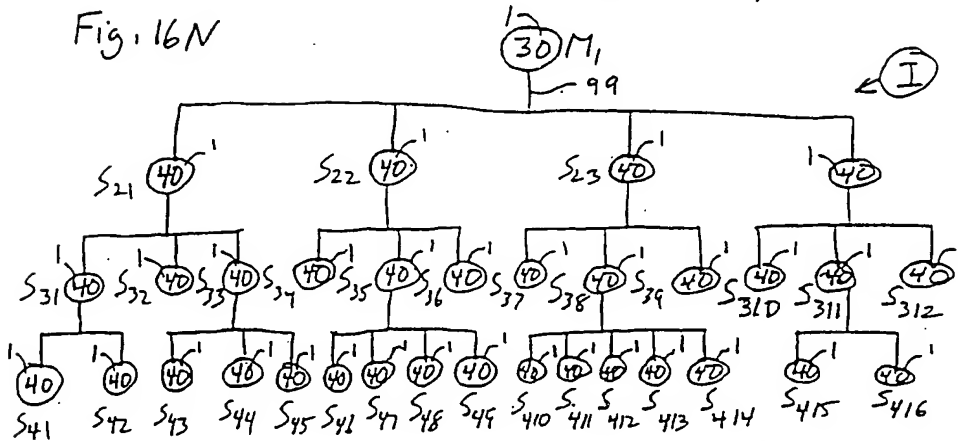


Fig. 16O

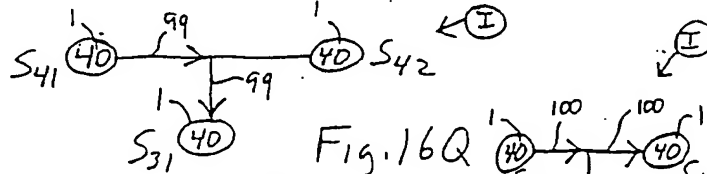


Fig. 16Q

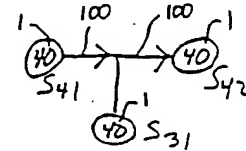
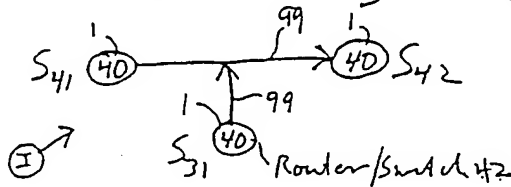


Fig. 16P



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Fig. 16X

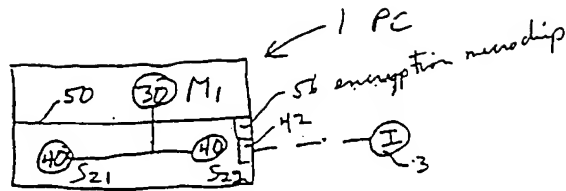


Fig. 16Y

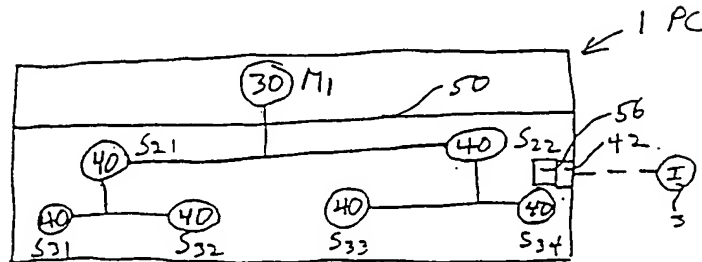


Fig. 16Z

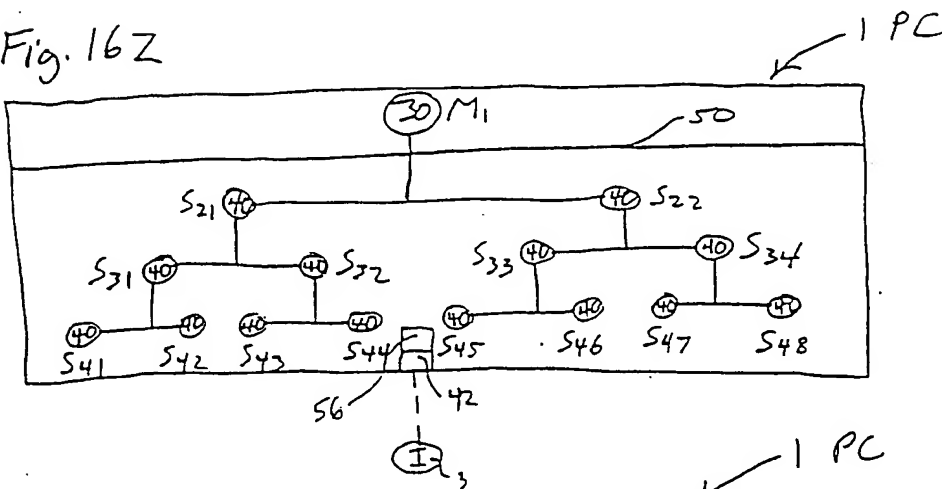
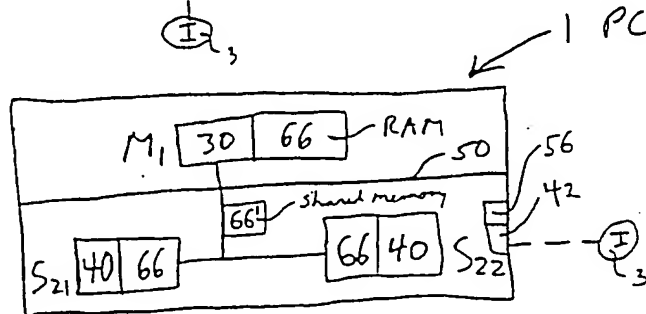


Fig. 16AA



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Fig. 16R

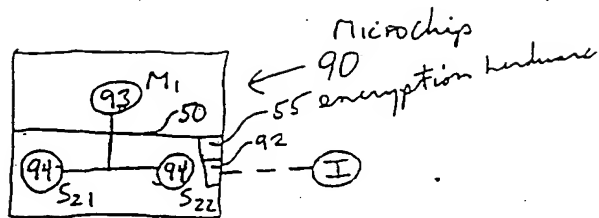


Fig. 16S

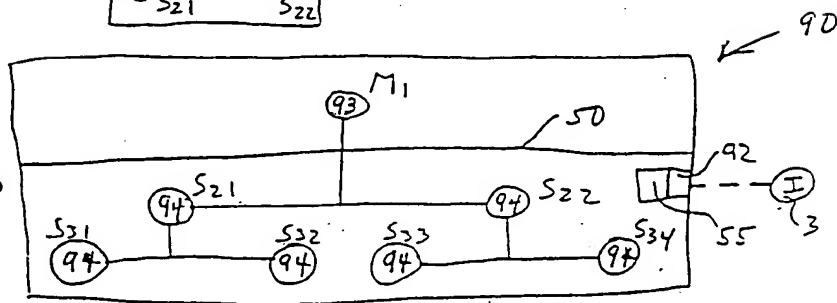


Fig. 16T

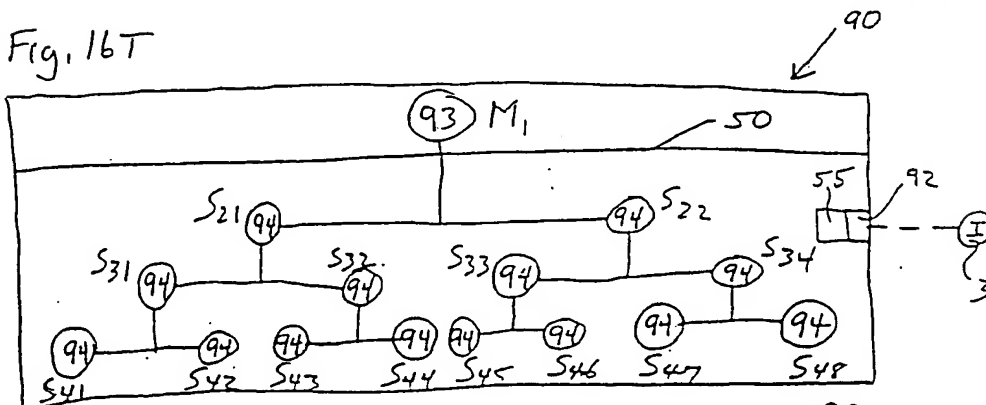
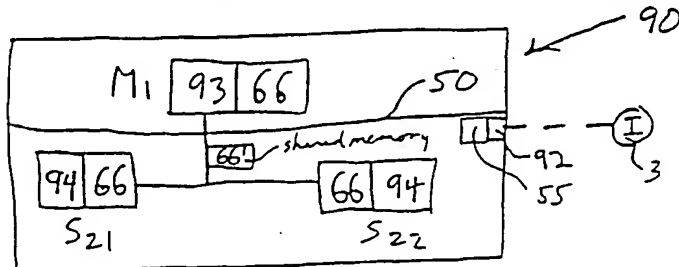
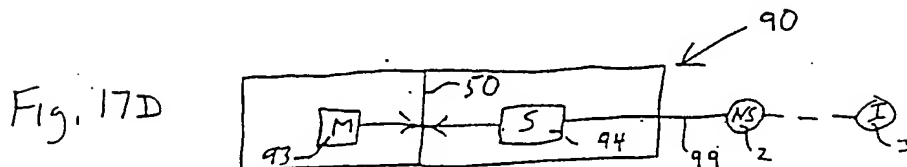
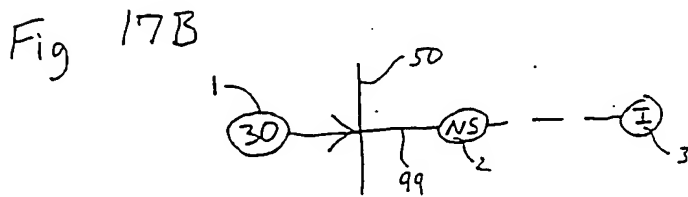
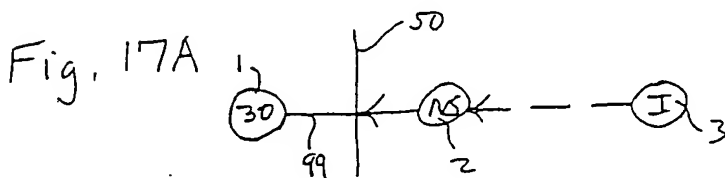
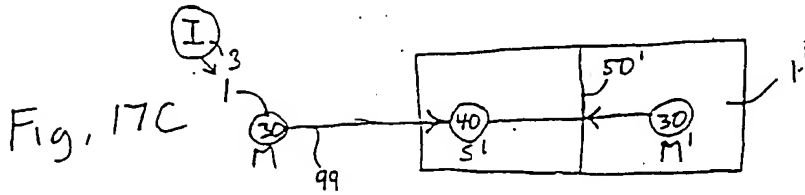
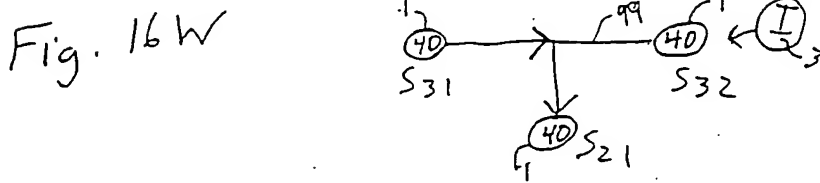
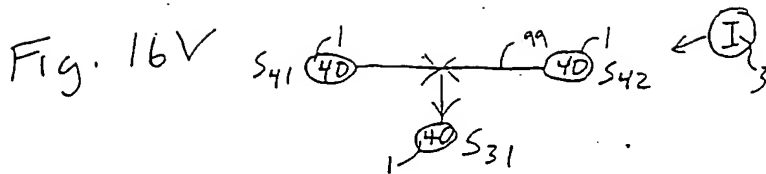


Fig. 16U



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Fig. 18A

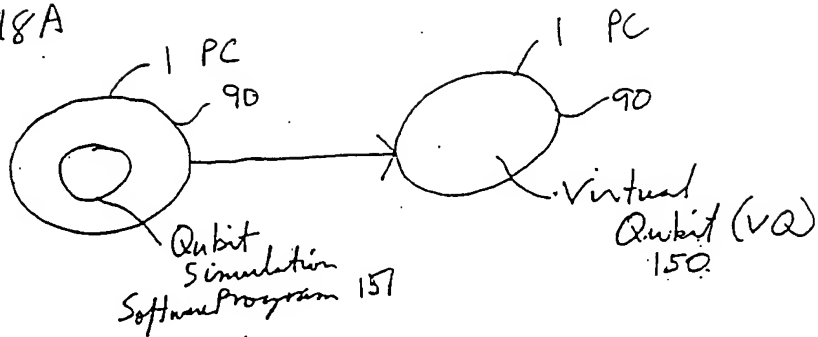


Fig. 18B

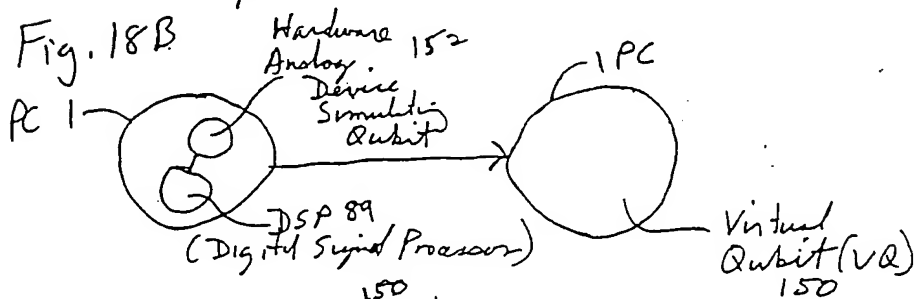


Fig. 18C

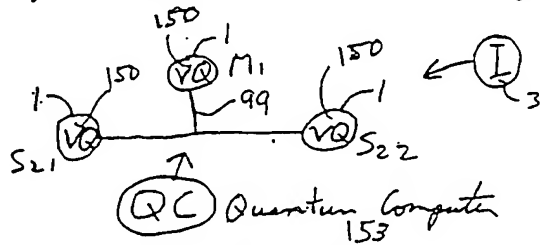
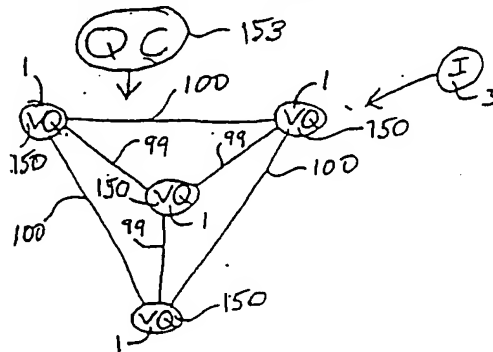


Fig. 18D



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Fig. 19

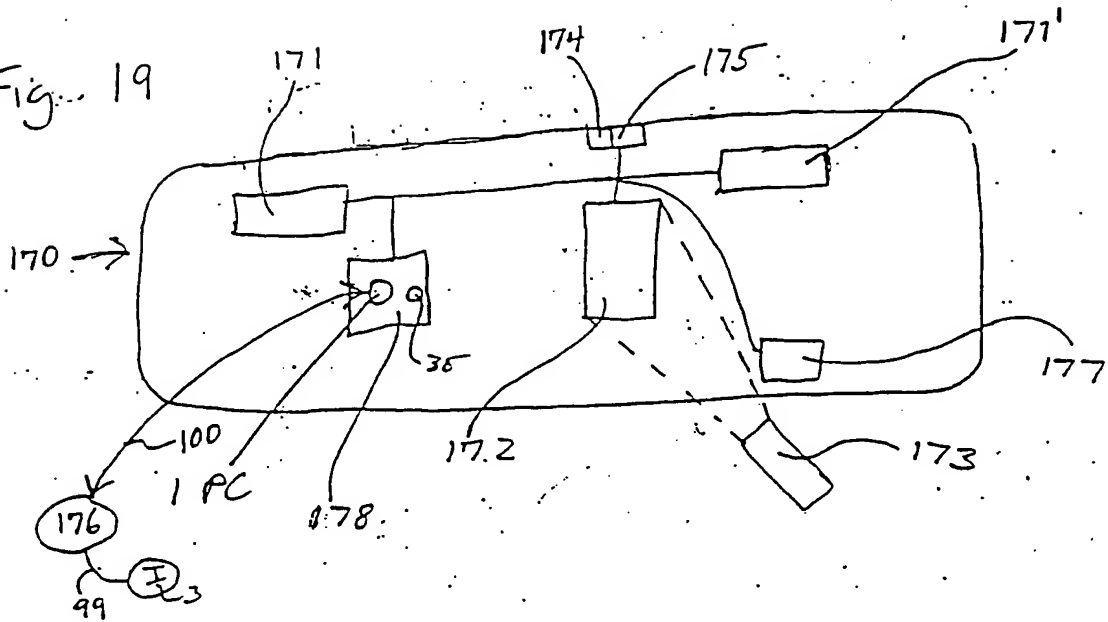


Fig. 16 A B

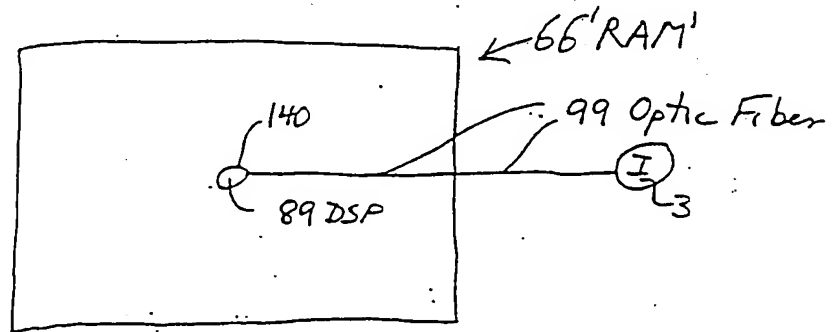
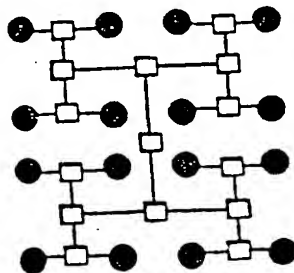


Fig.
21C



$$\begin{array}{r} 17 \overline{) 26} \\ \underline{31} \end{array}$$

Fig. 20A

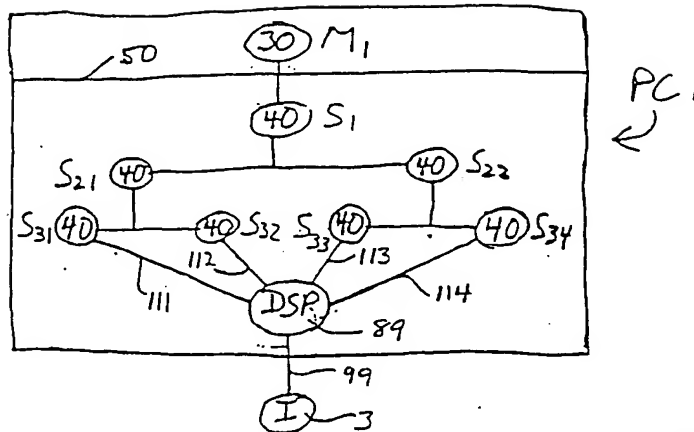
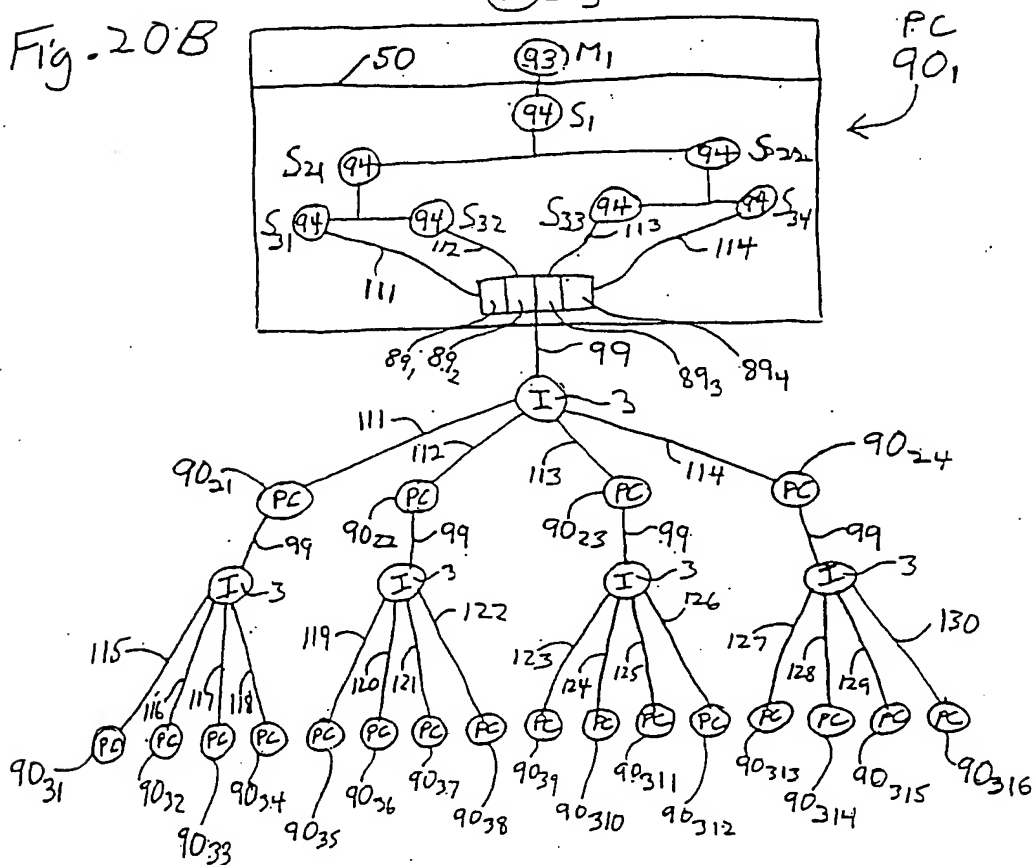


Fig. 20B



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Fig. 21A

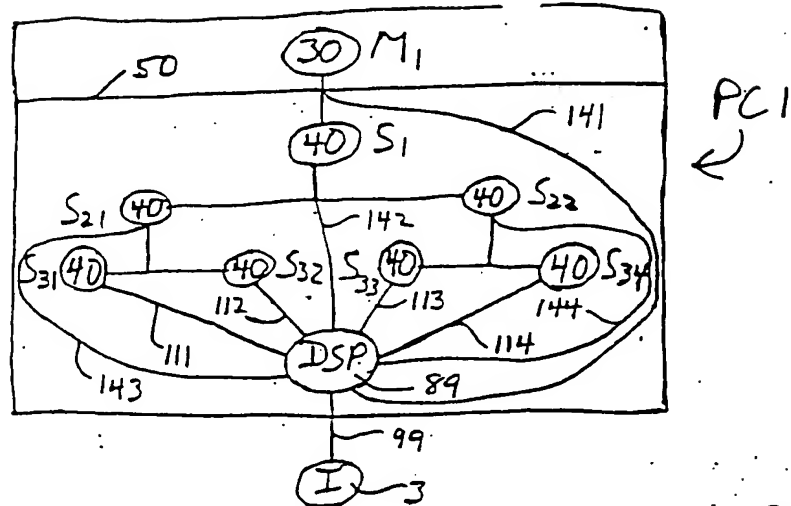
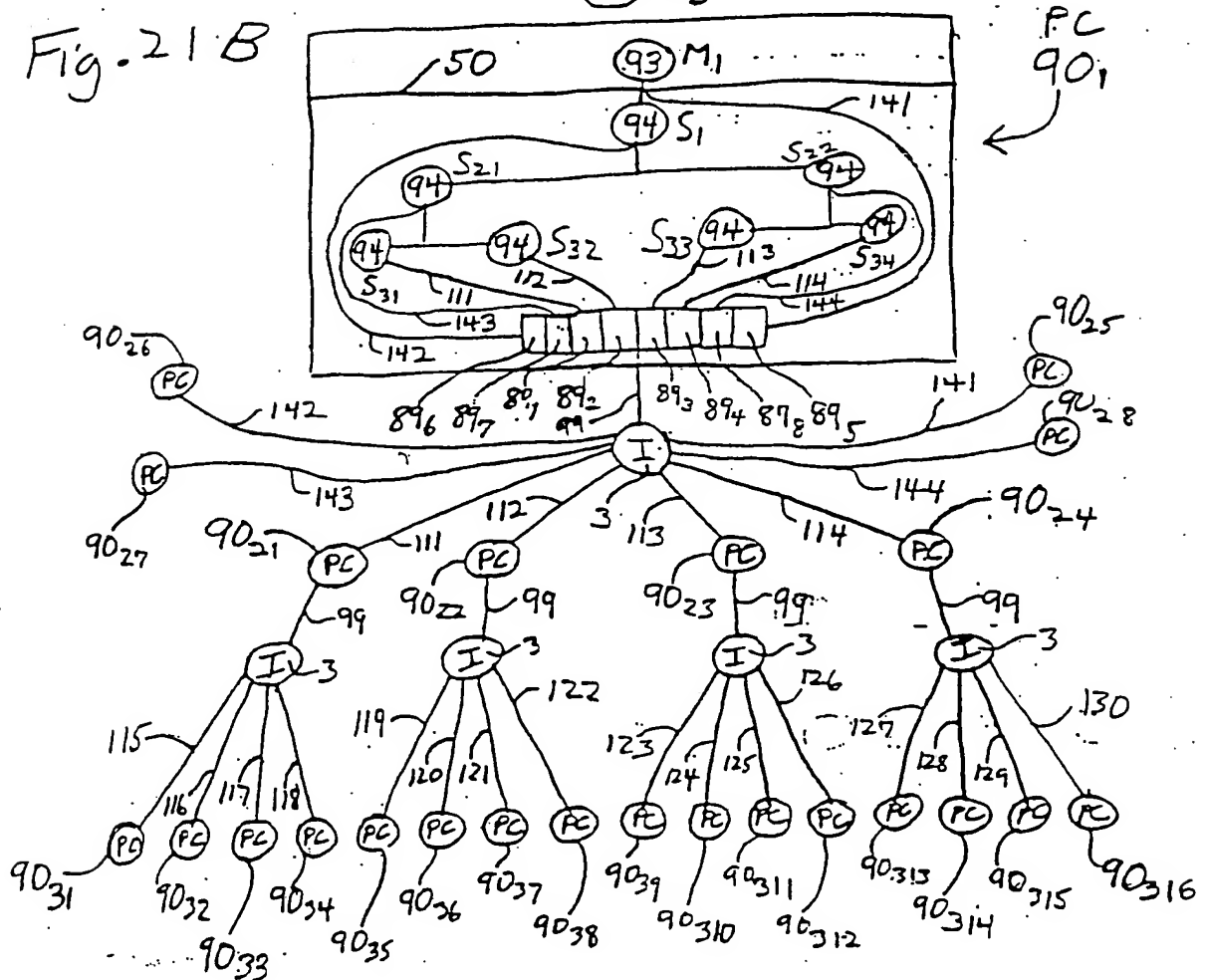


Fig. 21B



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Fig. 22A

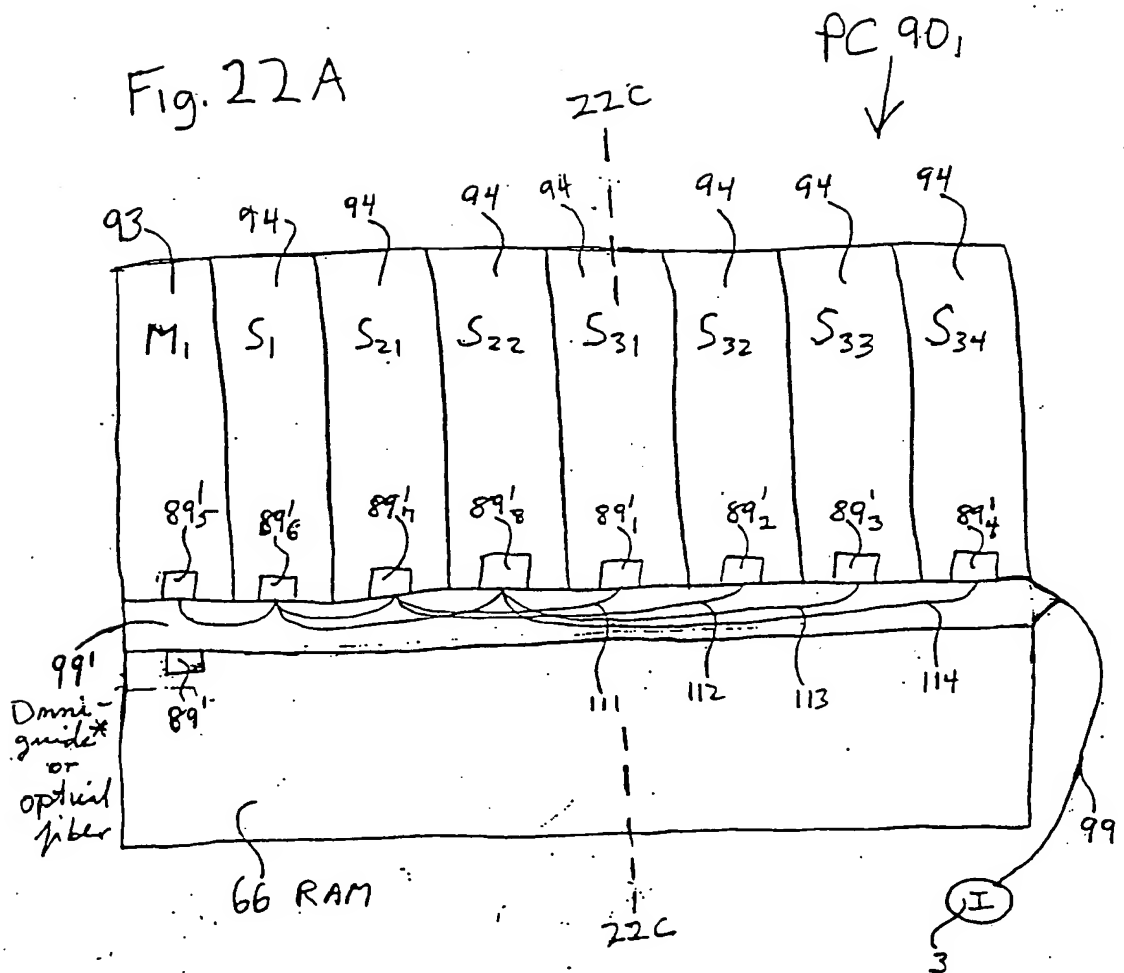


FIG. 22B

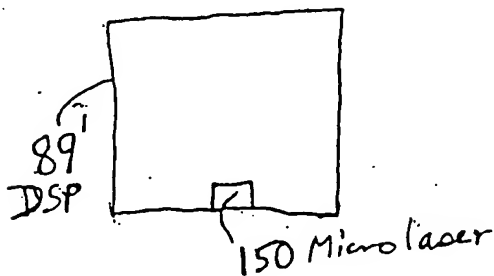
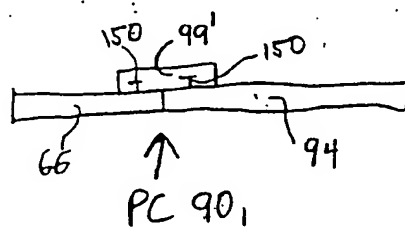


FIG. 22C



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Fig. 23A

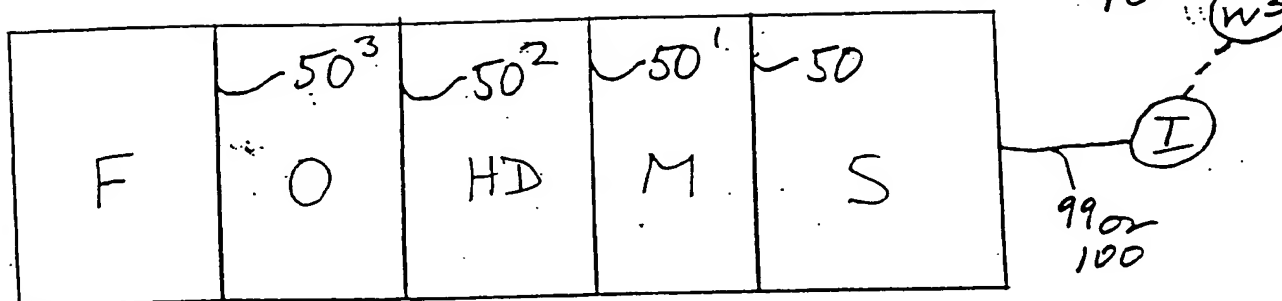
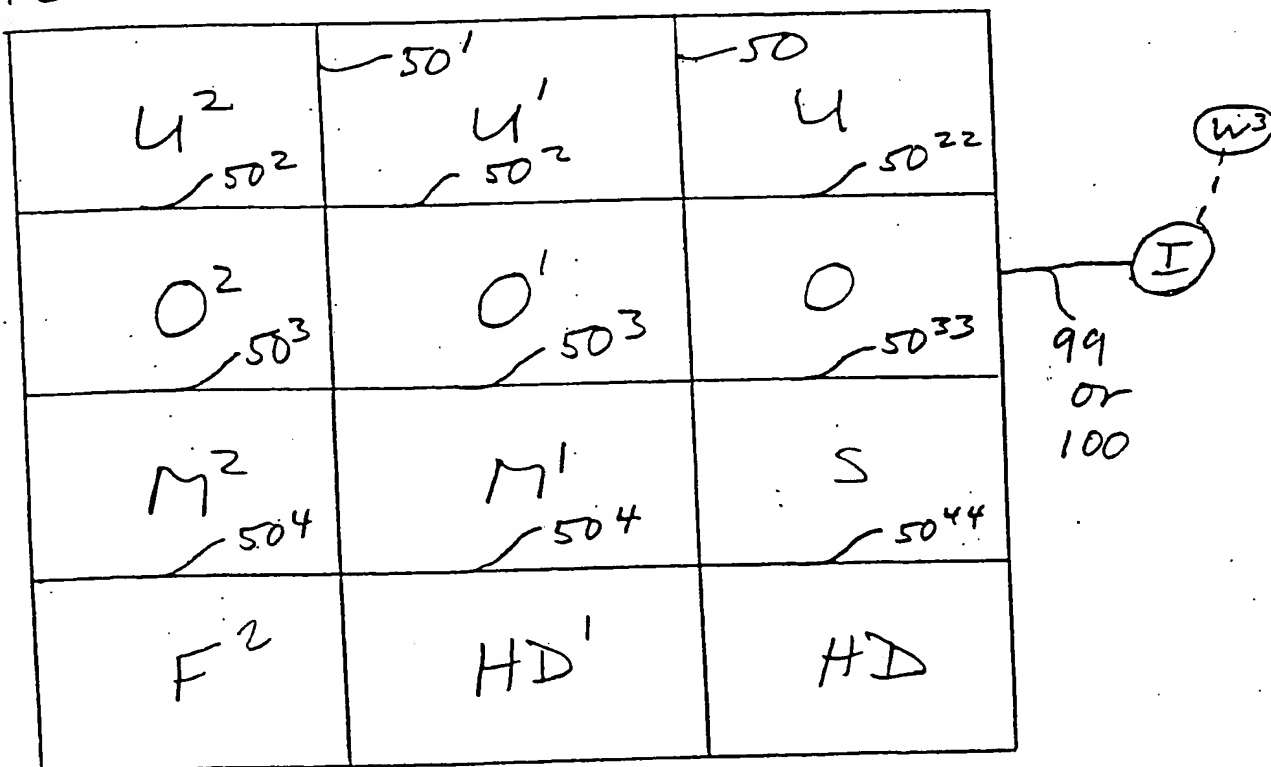


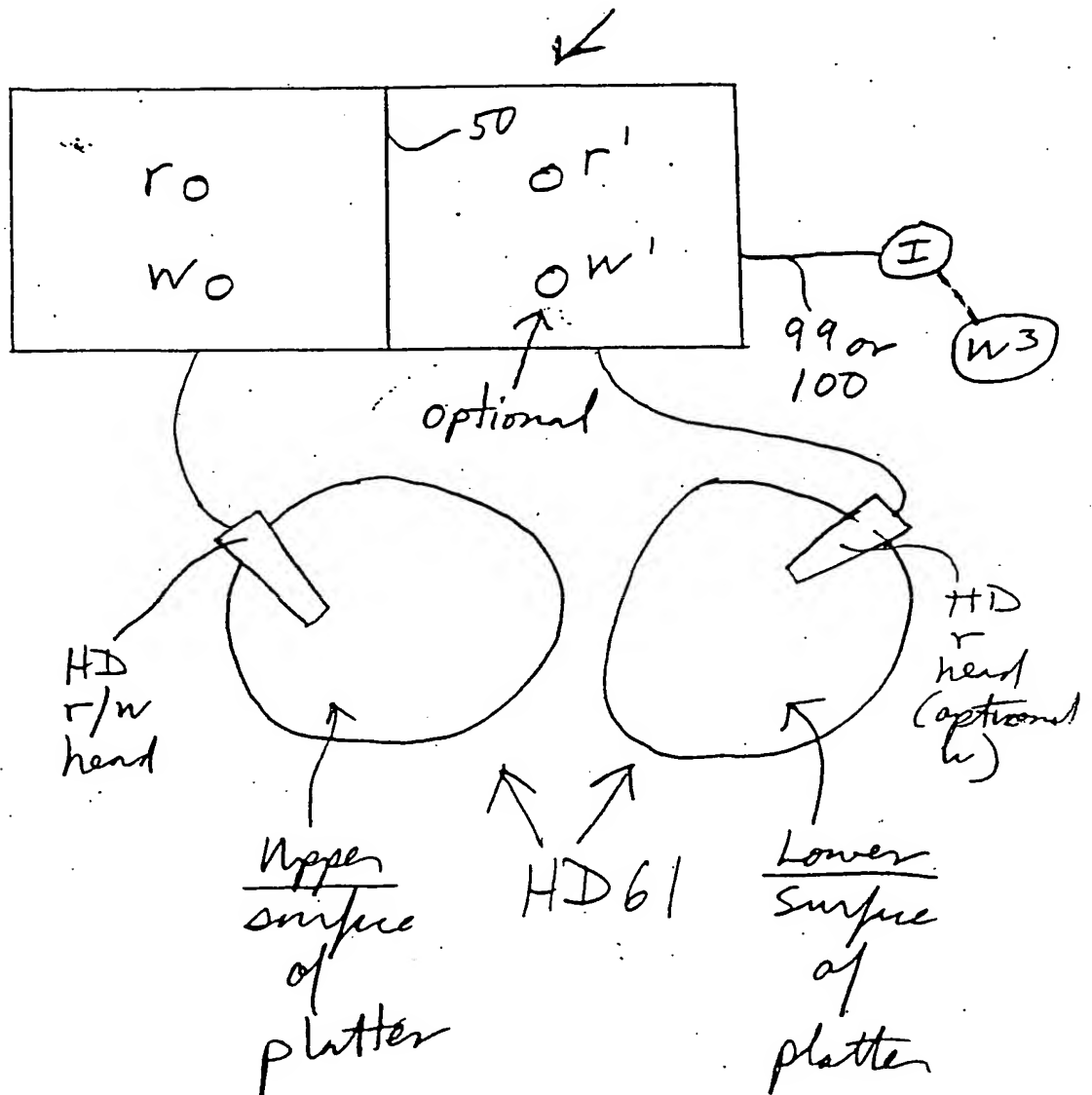
Fig. 23B



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FIG. 24

HD61 Controller



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PC1 or PC 90

Fig. 23C

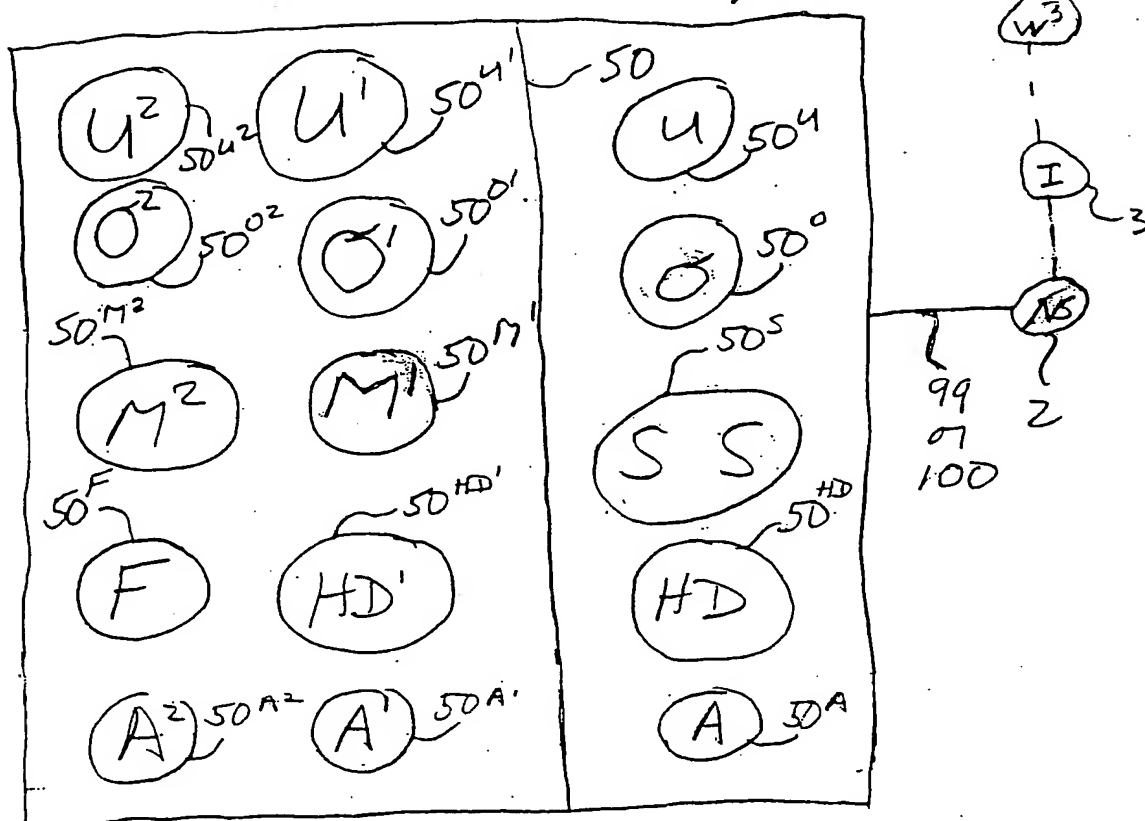


Fig. 23D

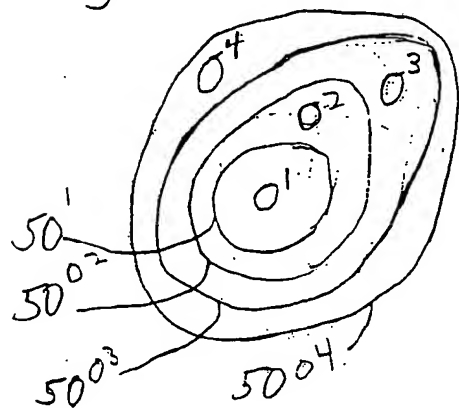


Figure 23E

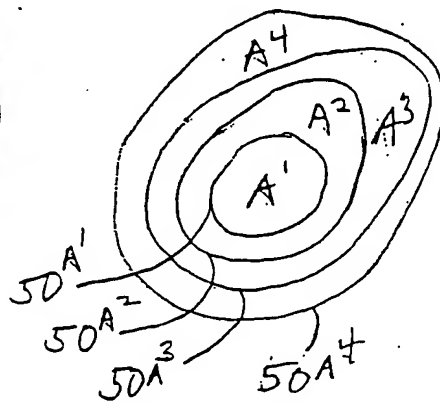


Fig. 25A

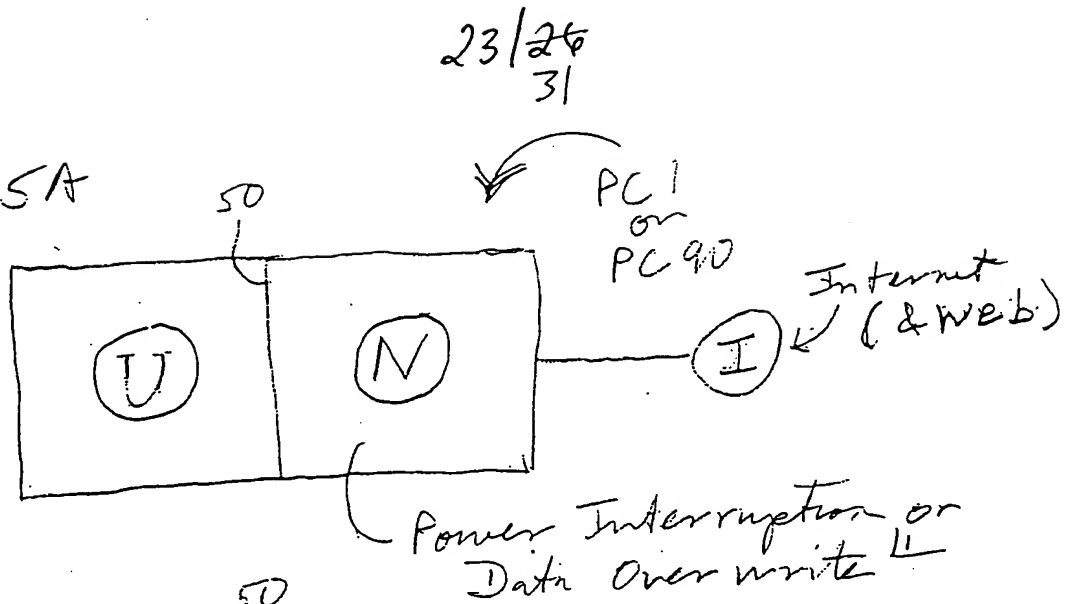


Fig. 25B

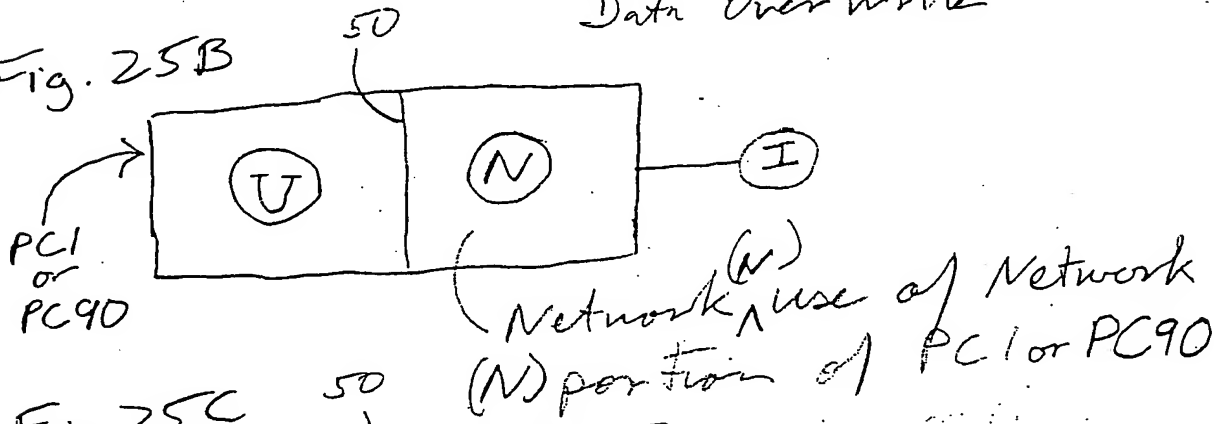


Fig. 25C

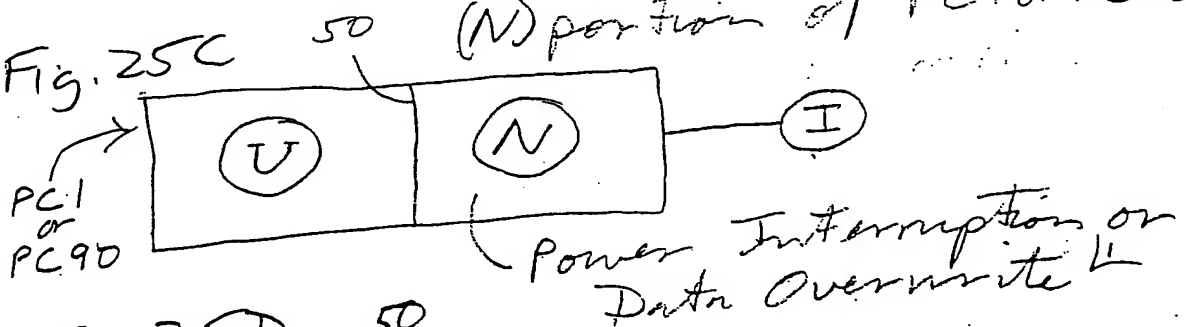
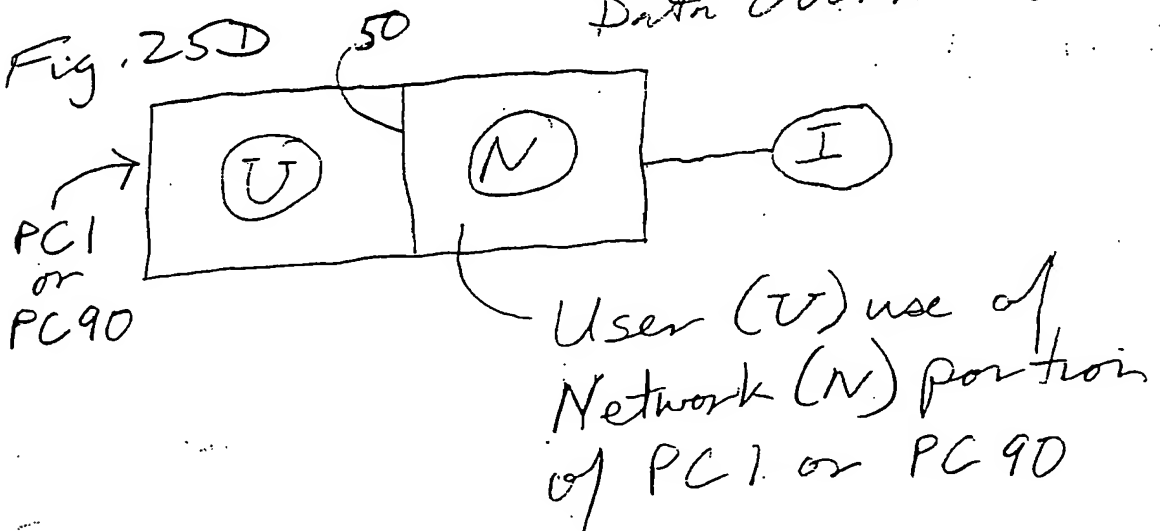


Fig. 25D



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Fig. 26A

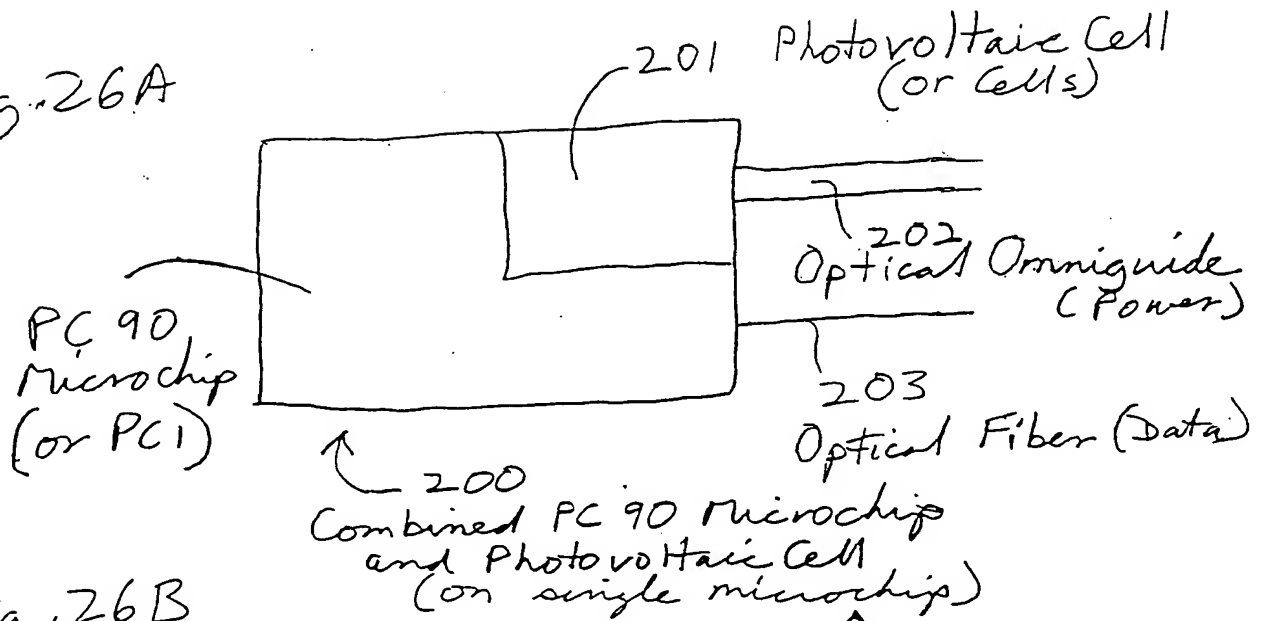


Fig. 26B

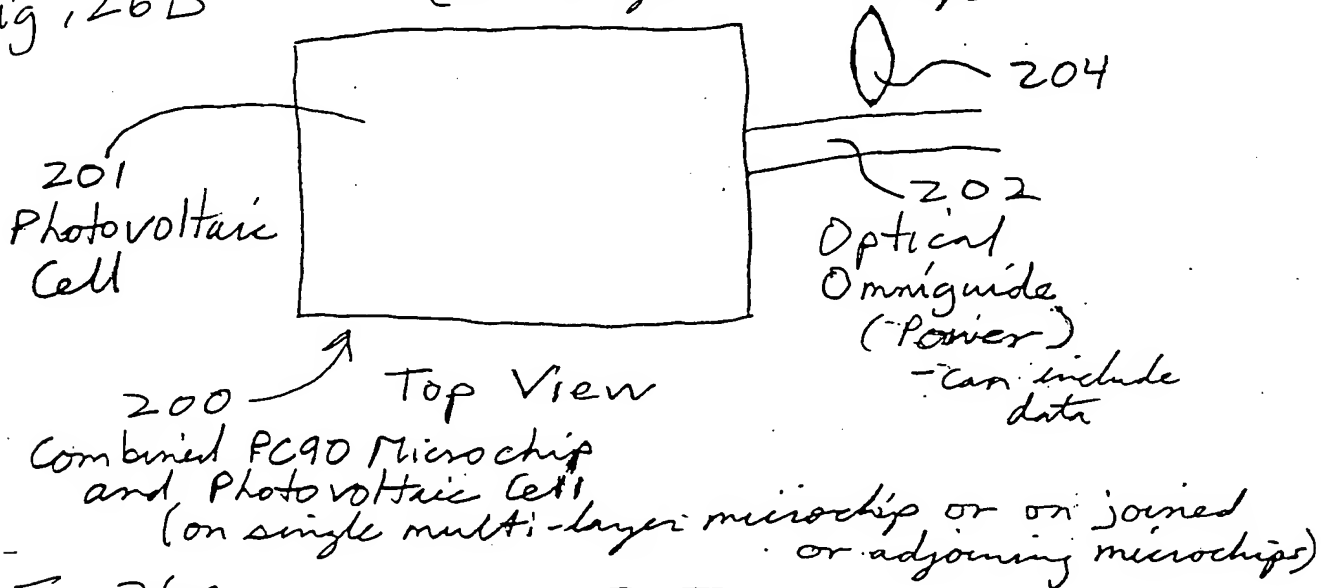
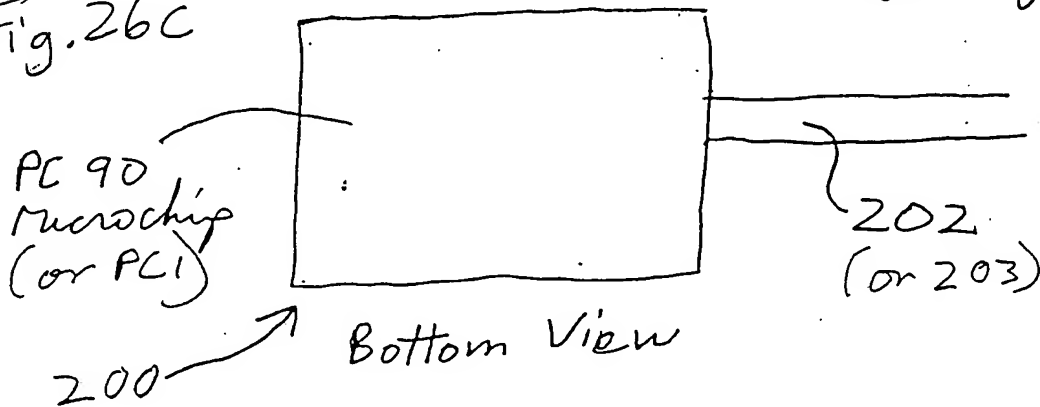


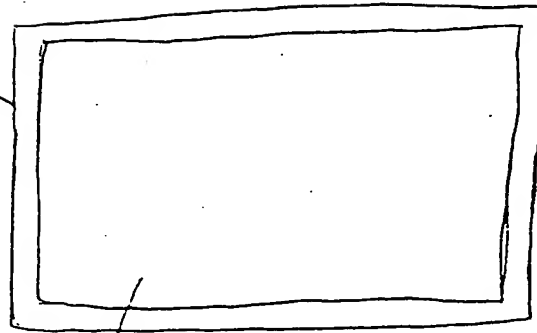
Fig. 26C



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Fig. 27A

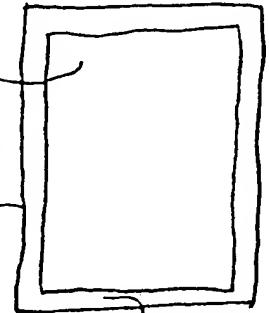
300
Faraday
Cage



PC90

Fig. 27G

300

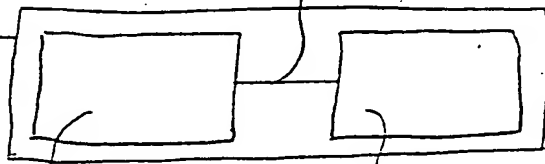


301

200 Combined PC90 Microchip
and Photovoltaic Cell 201

Fig. 27B

300
Faraday
Cage



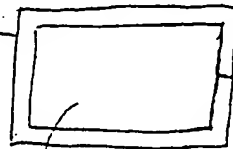
99 Wire

PC90 Microchip

201 Photovoltaic Cell (or Cells)

Fig. 27C

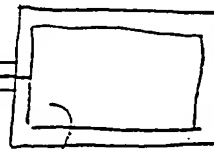
300
Faraday
Cage
(#1)



PC90
Microchip

300
(#3)

99



300
Faraday
Cage
(#2)

201 Photovoltaic Cell (or Cells)

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Fig. 27D

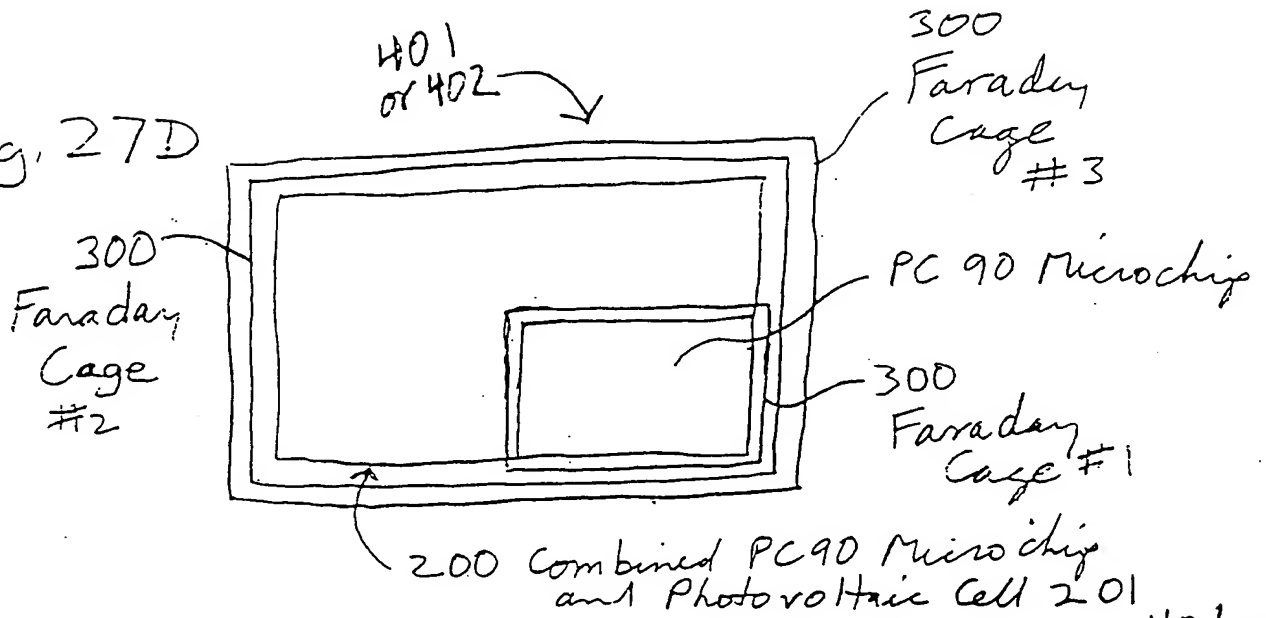


Fig. 27E

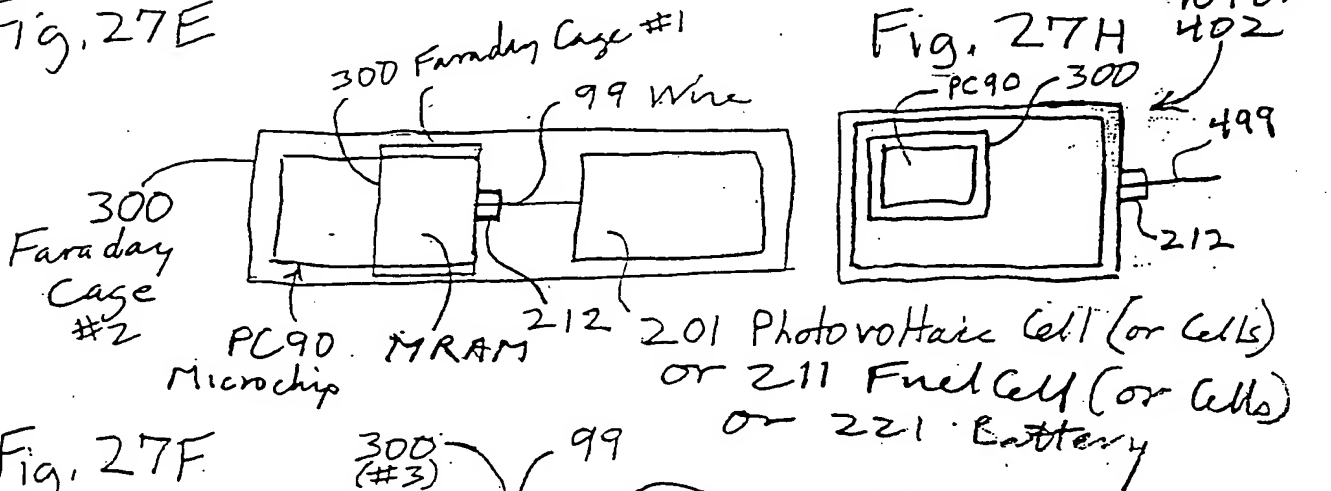
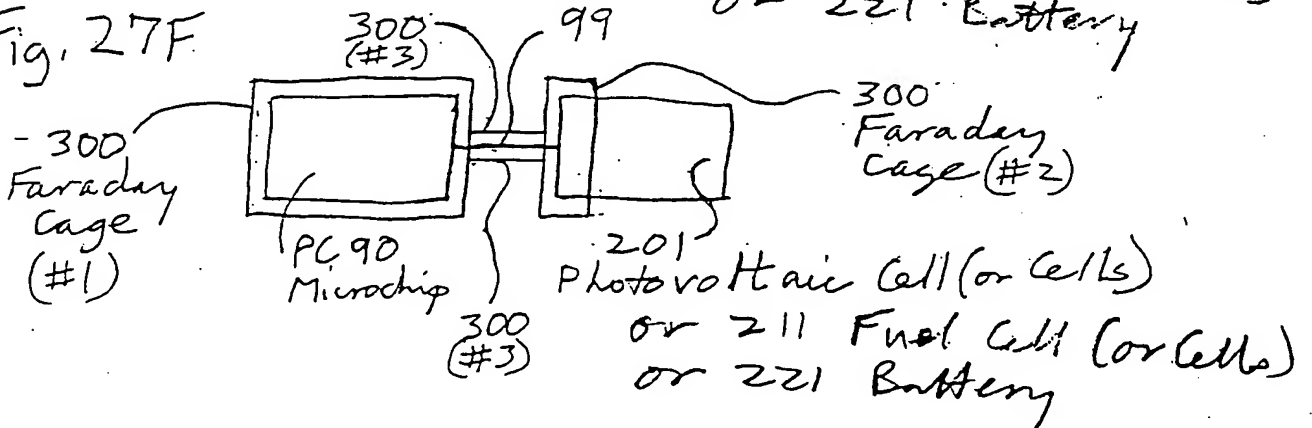
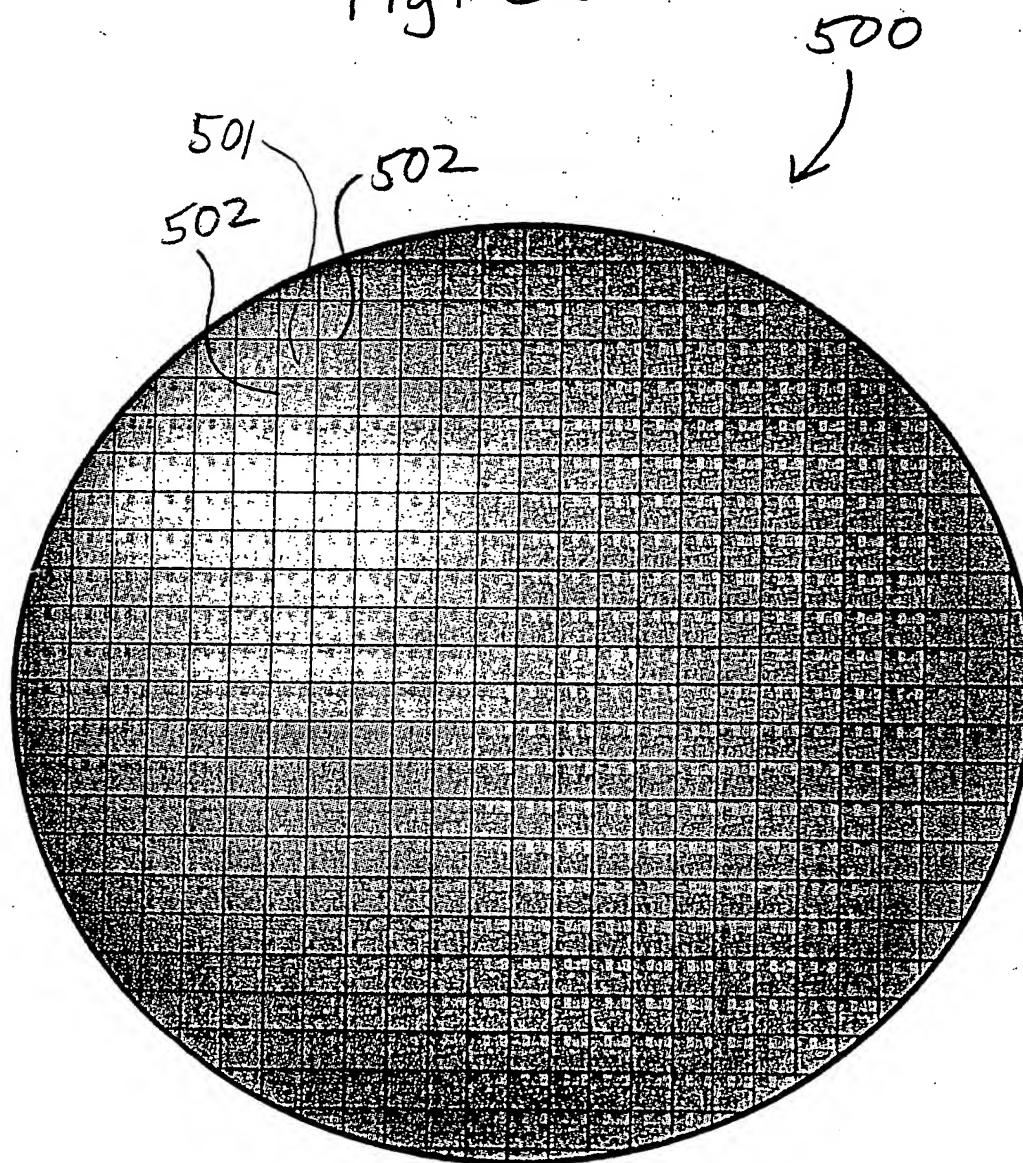


Fig. 27F



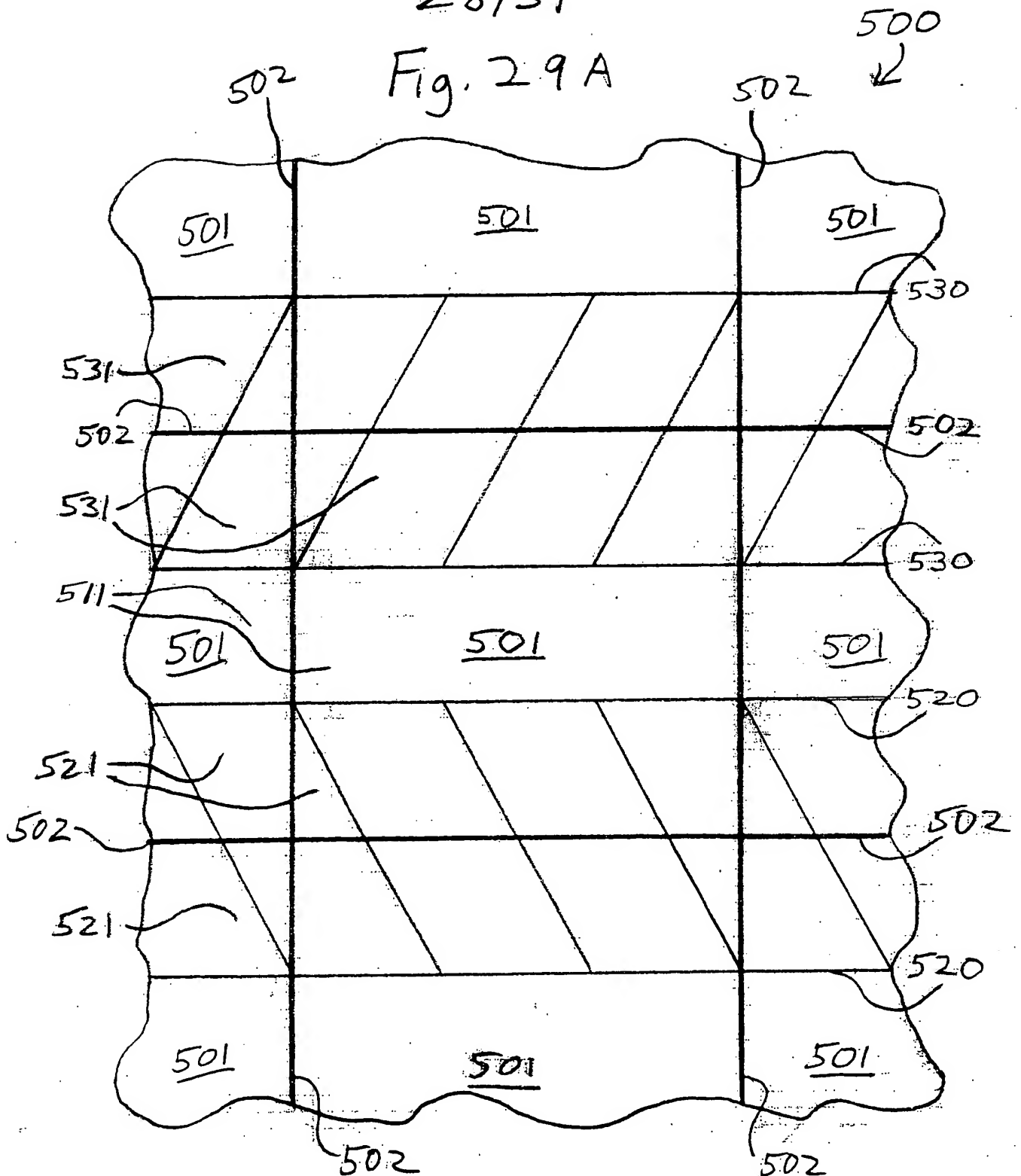
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Fig. 28

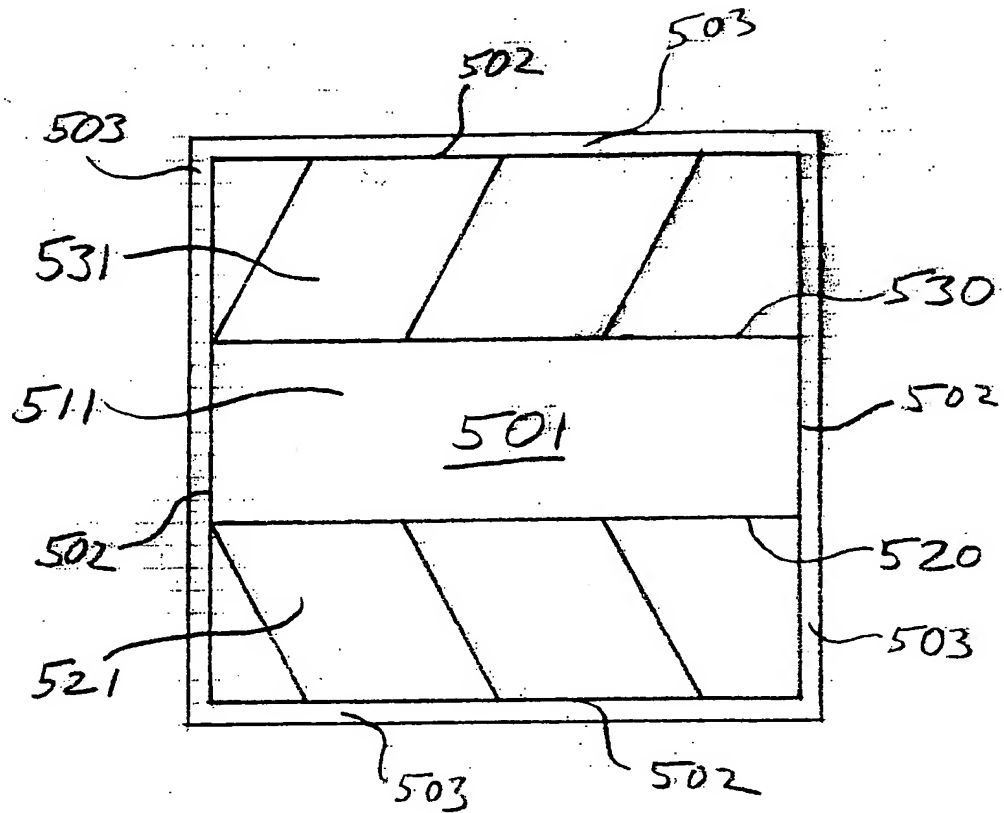


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Fig. 29A



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Fig. 29B



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Fig. 30A

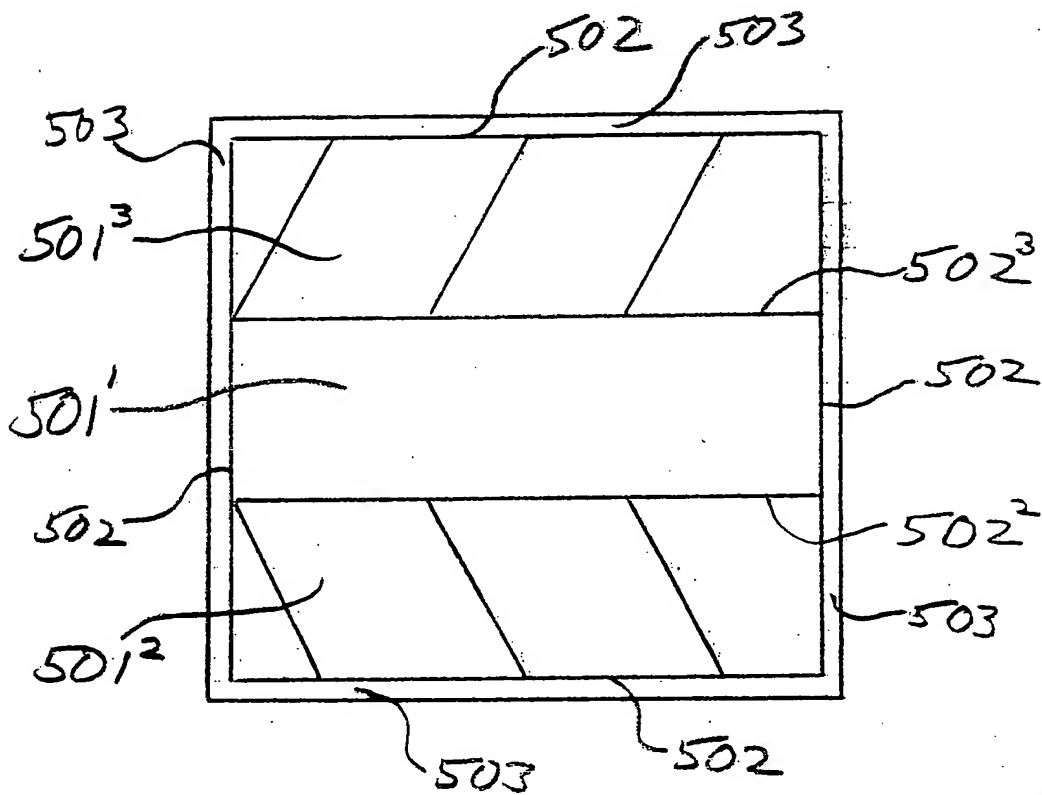


Fig. 30B

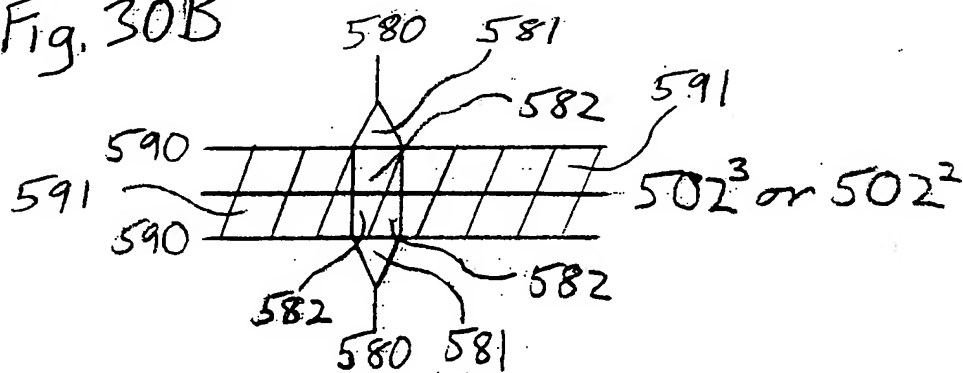


Fig. 30C



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Fig. 31

